

Amendments to the Claims

This listing of claims replaces all prior versions and listings of claims in the application.

Listing of Claims

1-10. (Canceled)

11. (Currently Amended) A signal line driver circuit comprising:

a shift register;

a latch circuit, electrically connected to the shift register, comprising a plurality of unit circuits, wherein each of the plurality of unit circuits comprises:

a ~~plurality of pairs~~ pair of current source circuits, wherein each of the ~~plurality of pairs of~~ current source circuits is configured to receive a set signal and a signal current, and to control a value of an output current ~~[[value]]~~ depending on a value of the signal current;

a ~~plurality of first switches~~ switch, wherein ~~each of the plurality of first switch circuits~~ is configured to select ~~one of one~~ a current source circuit from the pair of current source circuits among the plurality of pairs of current circuits for receiving the signal current; and

a ~~plurality of second switches~~ switch, wherein ~~each of the plurality of second switch circuits~~ is configured to select ~~the other one of the one~~ a current source circuit from the pair of current source circuits among the plurality of pairs of current circuits for outputting the output current; and

a changing over circuit electrically connected to the plurality of ~~pairs of current source circuits~~ unit circuits and a plurality of signal lines,

wherein the changing over circuit is configured to select ~~one pair of current source circuits~~ one unit circuit from the plurality of ~~pairs of current source unit circuits~~ for electrically connecting to each of the plurality of signal lines, and

wherein the shift register is configured to output the set signal.

12. (Currently Amended) A signal line driver circuit comprising:

a shift register;

a latch circuit, electrically connected to the shift register, comprising a plurality of unit circuits, wherein each of the plurality of unit circuits comprises:

~~a plurality of pairs~~ pair of current source circuits, wherein each of the ~~plurality of pairs of~~ current source circuits is configured to receive a set signal and a signal current, and to control a value of an output current ~~[[value]]~~ depending on a value of the signal current;

a first switch provided between the shift register and each of the ~~plurality of pairs~~ pair of current source circuits, the first switch being configured to select a current source circuit from the pair of current source circuits for receiving the signal current; and

a second switch, the second switch being configured to select a current source circuit from the pair of current source circuits for outputting the output current, and

a changing over circuit electrically connected between the plurality of ~~pairs of current source circuits~~ unit circuits and a plurality of signal lines,

wherein the changing over circuit is electrically connected to a particular ~~pair of current source circuits~~ unit circuit through the second switch,

wherein the changing over circuit is configured to select one ~~pair of current source circuits~~ unit circuit from the plurality of ~~pairs of current source circuits~~ unit circuits for electrically connecting to each of the plurality of signal lines,

wherein the shift register is configured to output the set signal, and

wherein the first and second switches are configured to be controlled based on a same latch pulse.

13-80. (Canceled)

81. (Currently Amended) A signal line driver circuit comprising:

a plurality of current source circuits, wherein each of the plurality of current source circuits is configured to ~~be supplied with~~ receive a first current and to supply a second current, and wherein a value of the second current depends on a value of the first current;

a plurality of first switches, wherein each of the plurality of first ~~switch-circuits~~ switches is configured to select ~~one of one pair~~ a current source circuit from the plurality of current source circuits among the plurality of pairs of current circuits for receiving the first current;

a plurality of second switches, wherein each of the plurality of second ~~switch-circuits~~ switches is configured to select ~~the other one of the one pair~~ a current source circuit from the plurality of current source circuits among the plurality of pairs of current circuits for supplying the second current;

a plurality of signal lines; and

a selector circuit configured to select one of the plurality of signal lines to which the second current is supplied.

82-91. (Canceled)

92. (Currently Amended) The signal line driver circuit according to claim 11,
wherein each of the ~~plurality of pairs~~ pair of current source circuits includes a transistor having a gate, a source and a drain and a capacitor having one electrode electrically connected to the source of the transistor and the other electrode electrically connected to the gate of the transistor, and

wherein each of the ~~plurality of pairs~~ pair of current source circuits is configured to control ~~[[an]] the value of the~~ output current ~~[[value]]~~ depending on a voltage between the gate and the source of the transistor of the pair of current source circuits that is generated by supplying ~~[[a]] the~~ signal current to the transistor while the gate and the drain of the transistor are electrically connected to each other.

93. (Currently Amended) The signal line driver circuit according to claim 12,
wherein each of the ~~plurality of pairs~~ pair of current source circuits includes a transistor having a gate, a source and a drain and a capacitor having one electrode electrically connected to the source of the transistor and the other electrode electrically connected to the gate of the transistor, and

wherein each of the ~~plurality of pairs~~ pair of current source circuits is configured to control ~~[[an]]~~ the value of the output current ~~[[value]]~~ depending on a voltage between the gate and the source of the transistor of the pair of current source circuits that is generated by supplying ~~[[a]]~~ the signal current to the transistor while the gate and the drain of the transistor are electrically connected to each other.

94. (Currently Amended) The signal line driver circuit according to claim 81,
wherein each of the plurality of ~~pairs of~~ current source circuits includes a transistor having a gate, a source and a drain and a capacitor having one electrode electrically connected to the source of the transistor and the other electrode electrically connected to the gate of the transistor, and

wherein each of the plurality of ~~pairs of~~ current source circuits is configured to control ~~an output~~ the value of the second current ~~[[value]]~~ depending on a voltage between the gate and the source of the transistor of the pair of current source circuits that is generated by supplying a ~~signal~~ the first current to the transistor while the gate and the drain of the transistor are electrically connected to each other.

95-96. (Canceled)